III Year - II Semester

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VLSI LABORATORY

Note: The students are required to design the schematic diagrams using CMOS logic and to draw the layout diagrams to perform the following experiments using 130nm technology with the Industry standard EDA Tools.

List of Experiments:

i. Design and Implementation of an Universal Gates
ii. Design and Implementation of an Inverter
iii. Design and Implementation of Full Adder
iv. Design and Implementation of Full Subtractor
v. Design and Implementation of Decoder
vi. Design and Implementation of RS-Latch
vii. Design and Implementation of D-Latch
viii. Design and Implementation asynchronous counter
ix. Design and Implementation of static RAM cell
x. Design and Implementation of 8 bit DAC using R-2R latter network

Software Required:

i. Mentor Graphics Software / Equivalent Industry Standard Software.
ii. Personal computer system with necessary software to run the programs and to implement.